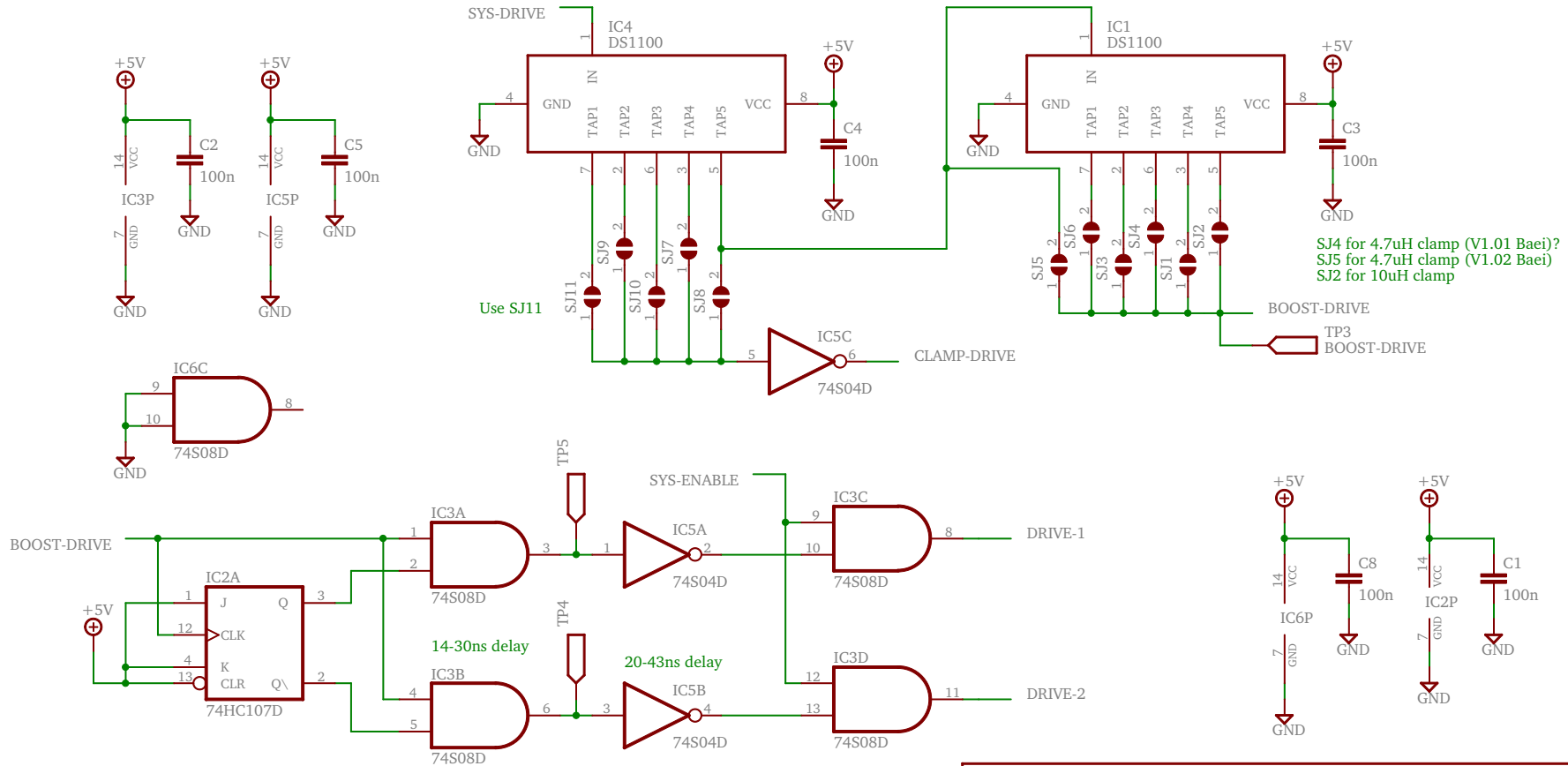


Can get rid of second DS1100 as not used when operating with 4.7uH clamp

7404 compat. hex inverter, Diodes Inc. 74HCT14S14-13, Digikey #74HCT14S14-13DICT-ND  
 7408 compat. quad 2-way AND gate, Diodes Inc. 74HCT08S14-13, Digikey #74HCT08S14-13DICT-ND  
 JK flip flop dual neg trig, NXP 74HC107D,653, Digikey #568-4552-1-ND  
 500ns delay chip, Maxim DS1100Z-500+, Digikey #DS1100Z-500+-ND

For input, influence, or ideas, special thanks to:

Matt Fox  
 Daniel Jury  
 Andrew Laphorn  
 Bart Wenmeckers  
 Ian Williamson



Discrete logic for boost circuit and clamp

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REV:

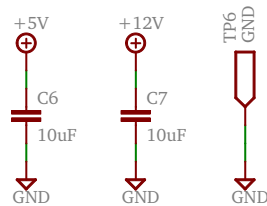
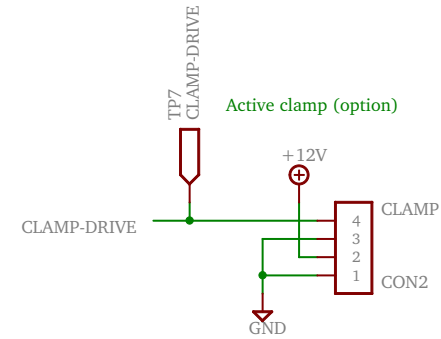
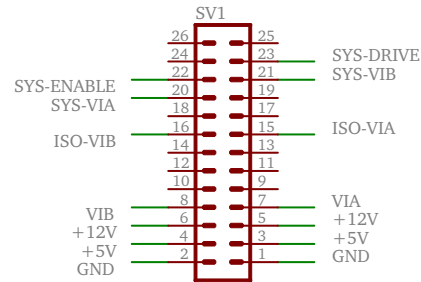
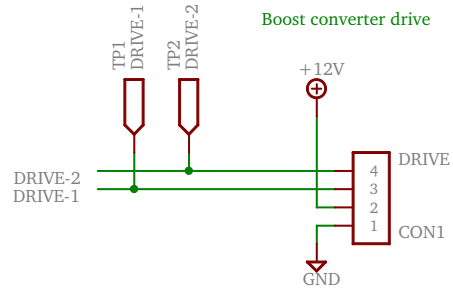
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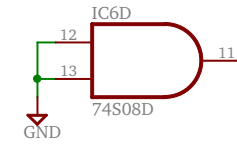
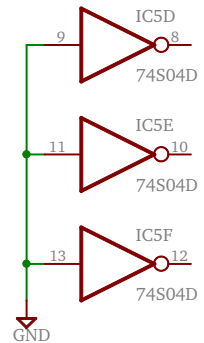
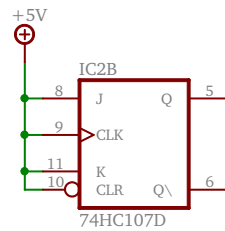
May not need 10uF on 12V which is a pass through anyway?

26-way header R/A, On Shore Technology Inc 302-R261, Digikey #ED10538-ND  
 26-way connector IDC, On Shore Technology Inc 101-266, Digikey #ED10505-ND



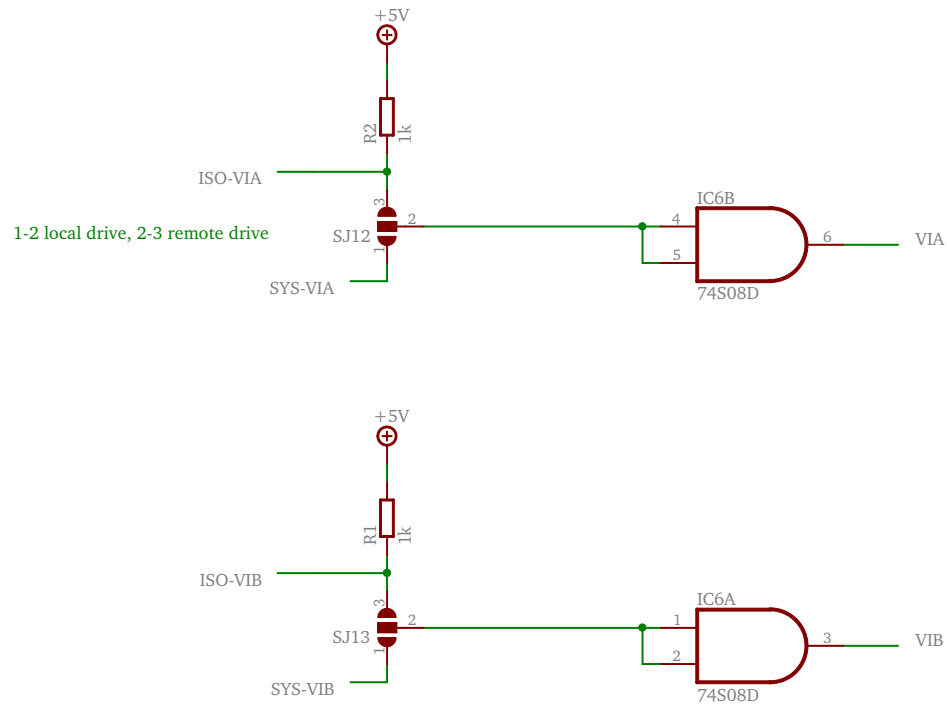
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Input/output connectors and PSU buffering	
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Unused gates	
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PWM load limiter	
TITLE: logic-v101	
Document Number:	REV:
Date: 2/19/2017 14:35:29	Sheet: 4/4